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(71) **Applicant** (for all designated States except US): **MAT-SUSHITA ELECTRIC INDUSTRIAL CO., LTD.**
[JP/JP]; 1006, Oaza Kadoma, Kadoma-shi, Osaka
5718501 (JP).

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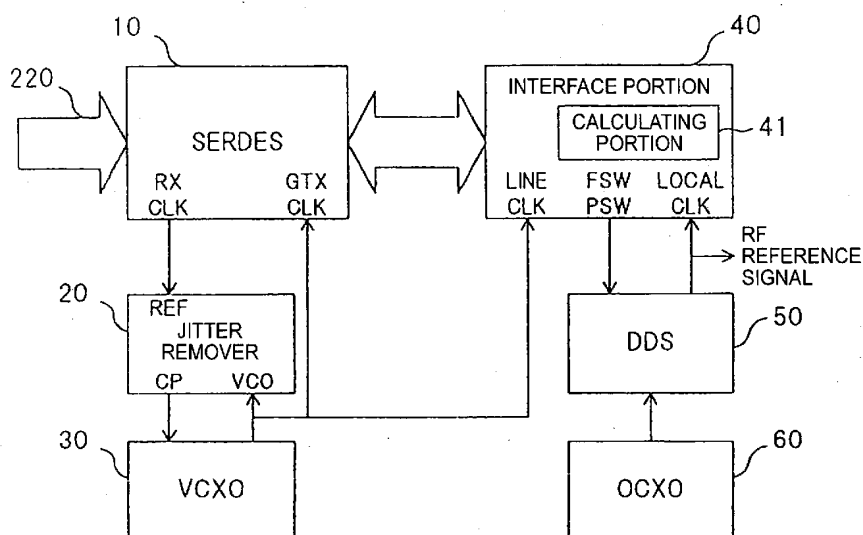
(72) Inventor; and
(75) Inventor/Applicant (for US only): ITAHARA, Hiroshi.

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(74) Agents: TAKAMATSU, Takeshi et al.; Eikoh Patent Office, 13th Floor, ARK Mori Building, 12-32, Akasaka 1-chome, Minato-ku, Tokyo 1076013 (JP).

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(54) Title: SYNCHRONOUS FOLLOW-UP APPARATUS AND SYNCHRONOUS FOLLOW-UP METHOD



(57) Abstract: A synchronous follow-up apparatus includes a PLL portion that outputs a first clock signal, and a control loop portion. The control loop portion includes a reference frequency signal generating portion that outputs a reference frequency signal, a clock signal generating portion that generates a second clock signal having the same frequency as a frequency of the first clock signal based on the reference frequency signal generated by the reference frequency signal generating portion, and a frequency control signal generating portion that generates a frequency control signal to change the frequency of the second clock signal based on a frequency difference between the first clock signal and the second clock signal that occurs after a predetermined time and outputs the frequency control signal to the clock signal generating portion.